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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR DESIGNING AN INTEGRATED CIRCUIT DEFECT MONITOR

Application Number :	
Date :	
First Named Applicant:	John M. Cohn
Confirmation Number:	
Attorney Docket Number:	BUR920040164US1

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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-assignment	BUR920040164US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	BUR920040164US1Assign1of3.tif
	BUR920040164US1Assign2of3.tif
	BUR920040164US1Assign3of3.tif
us-request	BUR920040164US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	BUR920040164US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	BUR920040164US1Dec1of3.tif
us-declaration	BUR920040164US1Dec2of3.tif
application-body	BUR920040164US1spec-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	BUR920040164US1Figs1of12.tif
	BUR920040164US1Figs2of12.tif
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us-declaration	BUR920040164US1Figs8of12.tif
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	BUR920040164US1Figs12of12.tif
	BUR920040164US1Dec3of3.tif

Comments